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- Equivalent Input Noise Voltage . . . 3.5 nV/√Hz Typ
- Unity-Gain Bandwidth . . . 10 MHz Typ
- Common-Mode Rejection Ratio . . . 100 dB Typ
- High DC Voltage Gain . . . 100 V/mV Typ
- Peak-to-Peak Output Voltage Swing 32 V Typ With V<sub>CC±</sub> = ±18 V and R<sub>L</sub> = 600 Ω
- High Slew Rate ... 13 V/μs Typ
- Wide Supply-Voltage Range ±3 V to ±20 V
- Low Harmonic Distortion
- Offset Nulling Capability
- External Compensation Capability

#### description/ordering information

NE5534, SA5534 ... D (SOIC), P (PDIP), OR PS (SOP) PACKAGE NE5534A, SA5534A ... D (SOIC) OR P (PDIP) PACKAGE (TOP VIEW) BALANCE  $\begin{bmatrix} 1 & 8 \\ 1 & 8 \end{bmatrix}$  COMP/BAL  $IN-\begin{bmatrix} 2 & 7 \\ 1 & 8 \end{bmatrix}$  COMP/BAL  $IN+\begin{bmatrix} 3 & 6 \\ 0 & 0 \end{bmatrix}$  OUT  $V_{CC-}$   $\begin{bmatrix} 4 & 5 \end{bmatrix}$  COMP

The NE5534, NE5534A, SA5534, and SA5534A are high-performance operational amplifiers combining excellent dc and ac characteristics. Some of the features include very low noise, high output-drive capability, high unity-gain and maximum-output-swing bandwidths, low distortion, and high slew rate.

These operational amplifiers are compensated internally for a gain equal to or greater than three. Optimization of the frequency response for various applications can be obtained by use of an external compensation capacitor between COMP and COMP/BAL. The devices feature input-protection diodes, output short-circuit protection, and offset-voltage nulling capability with use of the BALANCE and COMP/BAL pins (see the *application circuit* diagram).

For the NE5534A and SA5534A, a maximum limit is specified for the equivalent input noise voltage.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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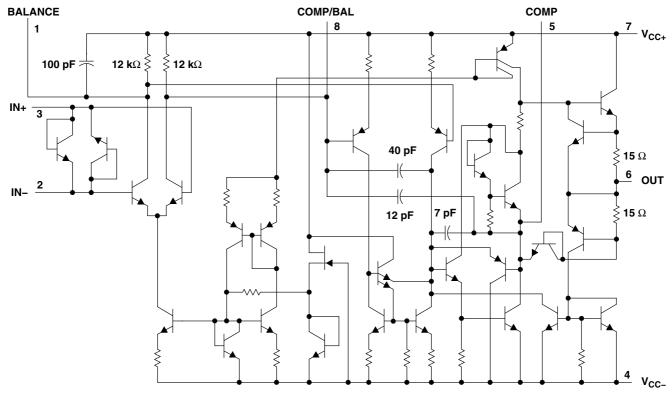
#### description/ordering information (continued)

T <sub>A</sub>	V <sub>IO</sub> max AT 25°C	PACKAGE <sup>†</sup>		ORDERABLE PART NUMBER	TOP-SIDE MARKING		
			Tube of 50		NE5534P		
		PDIP (P)	Tube of 50	NE5534AP	NE5534AP		
			Tube of 75	NE5534D			
0°C to 70°C	4 mV		Reel of 2500	NE5534DR	NE5534		
		SOIC (D)	Tube of 75	NE5534AD			
			Reel of 2500	NE5534ADR	5534A		
		SOP (PS)	Reel of 2000	NE5534PSR	N5534		
			Tube of 50	SA5534P	SA5534P		
		PDIP (P)	Tube of 50	SA5534AP	SA5534AP		
			Tube of 75	SA5534D	045504		
(220)			Reel of 2500	SA5534DR	SA5534		
–40°C to 85°C	4 mV	SOIC (D)	Tube of 75	SA5534AD	CA5504A		
			Reel of 2500	SA5534ADR	SA5534A		
			Tube of 80	SA553APS	045504		
		SOP (PS)	Reel of 2000	SA553APSR	SA5534		

#### **ORDERING INFORMATION**

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

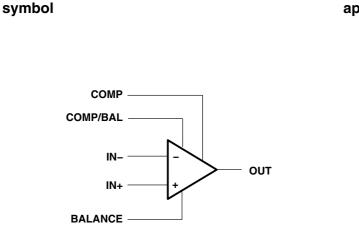
#### schematic

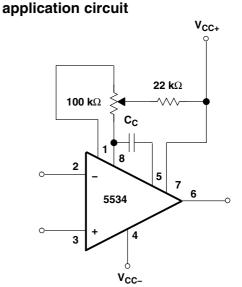


All component values shown are nominal.



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Frequency Compensation and Offset-Voltage Nulling Circuit

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

Supply voltage: V <sub>CC+</sub> (see Note 1)		22 V
V <sub>CC</sub> (see Note 1)		22 V
Input voltage either input (see Notes 1 and 2)		V <sub>CC+</sub>
Input current (see Note 3)		±10 mA
Duration of output short circuit (see Note 4)		Unlimited
Package thermal impedance, $\theta_{JA}$ (see Notes 5 and 6):	D package	97°C/W
	P package	85°C/W
	PS package	95°C/W
Operating virtual junction temperature, T <sub>J</sub>		150°C
Storage temperature range, T <sub>stg</sub>		. –65°C to 150°C

<sup>†</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to the midpoint between V<sub>CC+</sub> and V<sub>CC-</sub>
  - 2. The magnitude of the input voltage must never exceed the magnitude of the supply voltage.
  - 3. Excessive current will flow if a differential input voltage in excess of approximately 0.6 V is applied between the inputs, unless some limiting resistance is used.
  - 4. The output may be shorted to ground or to either power supply. Temperature and/or supply voltages must be limited to ensure the maximum dissipation rating is not exceeded.
  - 5. Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.
  - 6. The package thermal impedance is calculated in accordance with JESD 51-7.

#### recommended operating conditions

			MIN	MAX	UNIT
$V_{CC+}$	Supply voltage		5	15	V
$V_{CC-}$	Supply voltage		-5	-15	V
-		NE5534, NE5534A	0	70 °C	
Τ <sub>Α</sub>	Operating free-air temperature range	-40	85	°C	



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# electrical characteristics, $V_{CC}\pm$ = $\pm 15$ V, $T_A$ = 25°C (unless otherwise noted)

	PARAMETER	TEST CONDI	TIONS <sup>†</sup>	MIN	ТҮР	MAX	UNIT	
	hannah affa ak salka na	V <sub>O</sub> = 0,	$T_A = 25^{\circ}C$		0.5	4		
V <sub>IO</sub>	Input offset voltage	$R_{S} = 50 \Omega$	$T_A = Full range$			5	5 mV	
	han the floor han and		$T_A = 25^{\circ}C$		20	300		
l <sub>IO</sub>	Input offset current	V <sub>O</sub> = 0	T <sub>A</sub> = Full range			400	nA	
	Input biog gurrant	N O	$T_A = 25^{\circ}C$		500	1500	~^	
I <sub>IB</sub>	Input bias current	V <sub>O</sub> = 0	T <sub>A</sub> = Full range			2000	nA	
V <sub>ICR</sub>	Common-mode input voltage range			±12	±13		۷	
v			$V_{CC\pm} = \pm 15 V$	24	26		v	
V <sub>O(PP)</sub>	Maximum peak-to-peak output voltage swing	$R_L \ge 600 \ \Omega$	$V_{CC\pm} = \pm 18 \text{ V}$	30	32		V	
•		$V_{O} = \pm 10 V,$	$T_A = 25^{\circ}C$	25	100		V/mV	
A <sub>VD</sub>	Large-signal differential voltage amplification	$R_L \ge 600 \ \Omega$	T <sub>A</sub> = Full range	15			v/mv	
•	Creall signal differential value on analification	f = 10 kHz	C <sub>C</sub> = 0		6		\//ma\/	
A <sub>vd</sub>	Small-signal differential voltage amplification	T = 10 KHZ	C <sub>C</sub> = 22 pF		2.2		V/mV	
		V 10V	C <sub>C</sub> = 0		200	kHz		
B <sub>OM</sub>	Maximum-output-swing bandwidth	$V_O = \pm 10 V$	$C_C = 22 \text{ pF}$		95			
DOW	Maximum output owing banamatin	$\label{eq:V_CC_total} \begin{split} V_{CC\pm} &= \pm 18 \text{ V}, \\ R_L \geq 600 \ \Omega, \end{split}$	$V_O = \pm 14 V$ , $C_C = 22 pF$		70			
B <sub>1</sub>	Unity-gain bandwidth	C <sub>C</sub> = 22 pF,	C <sub>L</sub> = 100 pF		10		MHz	
r <sub>i</sub>	Input resistance			30	100		kΩ	
z <sub>o</sub>	Output impedance	$\begin{array}{l} A_{VD}=30 \text{ dB},\\ C_{C}=22 \text{ pF}, \end{array}$	R <sub>L</sub> ≥ 600 Ω, f = 10 kHz		0.3		Ω	
CMRR	Common-mode rejection ratio	$V_{O} = 0,$ $R_{S} = 50 \ \Omega$	$V_{IC} = V_{ICR}min^{-1}$	70	100		dB	
k <sub>SVR</sub>	Supply-voltage rejection ratio ( $\Delta V_{CC}\!/\!\Delta V_{IO)}$	$V_{CC+} = \pm 9 V \text{ to } \pm 15 V,$ $V_O = 0$	R <sub>S</sub> = 50 Ω,	80	100		dB	
l <sub>OS</sub>	Output short-circuit current				38		mA	
I <sub>CC</sub>	Supply current	V <sub>O</sub> = 0, No load	T <sub>A</sub> = 25°C		4	8	mA	

<sup>†</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified. For NE5534 and NE5534A, full range is 0°C to 70°C. For SA5534 and SA5534A, full range is –40°C to 85°C.



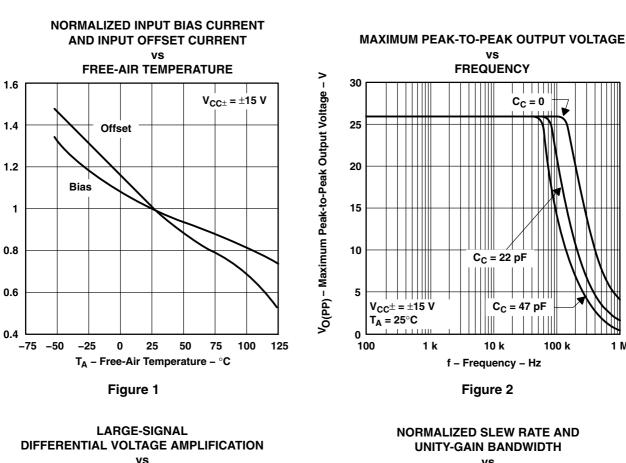
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	PARAMETER	TEST CONDITIONS	NE5534, SA5534	NE5534A, SA55	UNIT		
			TYP	MIN TYP	MAX		
0.0		C <sub>C</sub> = 0	13	13		N// -	
SR	Slew rate	C <sub>C</sub> = 22 pF	6	6	V/µs		
	Rise time	$V_{I} = 50 \text{ mV},  A_{VD} = 1,$ $R_{I} = 600 \Omega,  C_{C} = 22 \text{ pF}$	20	20		ns	
	Overshoot factor	$R_L = 600 \Omega$ , $C_C = 22 pF$ $C_L = 100 pF$	20	20		%	
t <sub>r</sub>	Rise time	$V_{I} = 50 \text{ mV},  A_{VD} = 1,$	50	50		ns	
	Overshoot factor	$R_L = 600 \Omega$ , $C_C = 47 pF$ $C_L = 500 pF$	35	35		%	
v		f = 30 Hz	7	5.5	7		
Vn	Equivalent input noise voltage	f = 1 kHz	4	3.5	4.5	nV/√Hz	
		f = 30 Hz	2.5	1.5			
In	Equivalent input noise current	f = 1 kHz	0.6	0.4		pA/√Hz	
F	Average noise figure	$R_S = 5 k\Omega$ , $f = 10 Hz$ to 20 kHz		0.9		dB	

### operating characteristics, $V_{CC}\,\pm$ = $\pm 15$ V, $T_{A}$ = $25^{\circ}C$

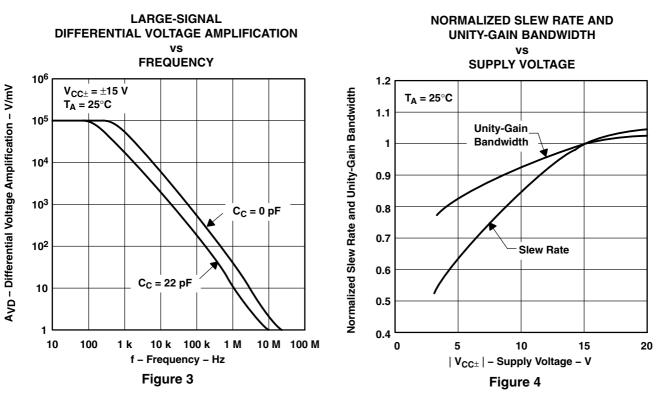


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### **TYPICAL CHARACTERISTICS<sup>†</sup>**

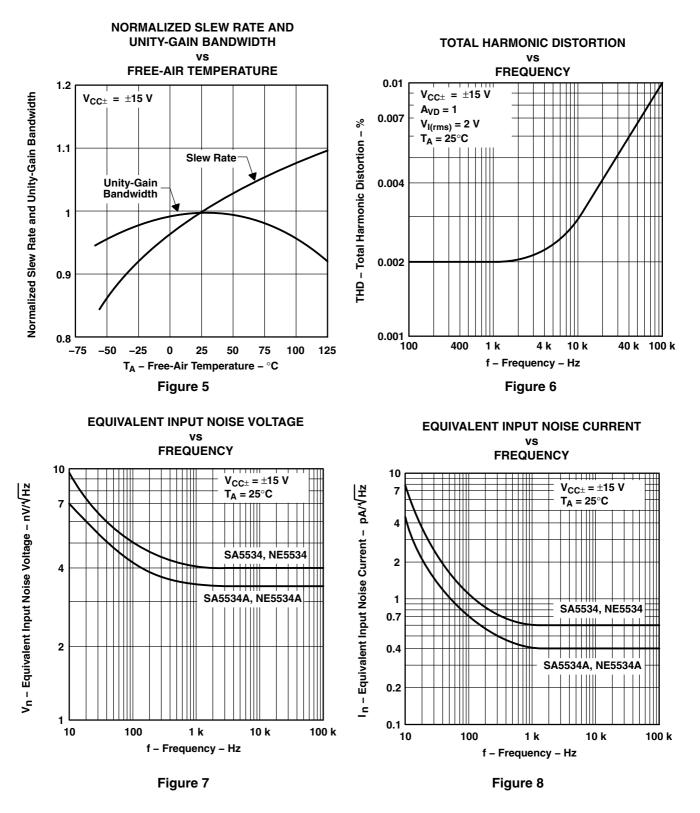


<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.



Normalized Input Bias Current and Input Offset Current

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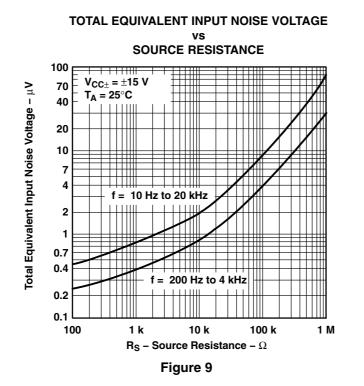


#### **TYPICAL CHARACTERISTICS<sup>†</sup>**

<sup>†</sup> Data at high and low temperatures are applicable only within the rated operating free-air temperature ranges of the various devices.



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#### **TYPICAL CHARACTERISTICS**





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#### **PACKAGING INFORMATION**

Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
NE5534AD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
NE5534ADE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
NE5534ADG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
NE5534ADR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributor or Sales Office
NE5534ADRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributor or Sales Office
NE5534ADRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributor or Sales Office
NE5534AJG	OBSOLETE	CDIP	JG	8		TBD	Call TI	Call TI	Samples Not Available
NE5534AP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
NE5534APE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
NE5534D	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributor or Sales Office
NE5534DE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributor or Sales Office
NE5534DG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributor or Sales Office
NE5534DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
NE5534DRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
NE5534DRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
NE5534IP	OBSOLETE	PDIP	Р	8		TBD	Call TI	Call TI	Samples Not Available
NE5534P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
NE5534PE4	ACTIVE	PDIP	Ρ	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office



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Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
NE5534PSR	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
NE5534PSRE4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
NE5534PSRG4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SA5534AD	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
SA5534ADE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
SA5534ADG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
SA5534ADR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributo or Sales Office
SA5534ADRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributo or Sales Office
SA5534ADRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributo or Sales Office
SA5534AP	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office
SA5534APE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office
SA5534D	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
SA5534DE4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
SA5534DG4	ACTIVE	SOIC	D	8	75	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Purchase Samples
SA5534DR	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributo or Sales Office
SA5534DRE4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributo or Sales Office
SA5534DRG4	ACTIVE	SOIC	D	8	2500	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-2-260C-1 YEAR	Contact TI Distributo or Sales Office
SA5534P	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributo or Sales Office



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Orderable Device	Status <sup>(1)</sup>	Package Type	Package Drawing	Pins	Package Qty	Eco Plan <sup>(2)</sup>	Lead/ Ball Finish	MSL Peak Temp <sup>(3)</sup>	Samples (Requires Login)
SA5534PE4	ACTIVE	PDIP	Р	8	50	Pb-Free (RoHS)	CU NIPDAU	N / A for Pkg Type	Contact TI Distributor or Sales Office
SA5534PS	ACTIVE	SO	PS	8	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SA5534PSE4	ACTIVE	SO	PS	8	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SA5534PSG4	ACTIVE	SO	PS	8	80	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Purchase Samples
SA5534PSR	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Contact TI Distributo or Sales Office
SA5534PSRE4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Contact TI Distributo or Sales Office
SA5534PSRG4	ACTIVE	SO	PS	8	2000	Green (RoHS & no Sb/Br)	CU NIPDAU	Level-1-260C-UNLIM	Contact TI Distributo or Sales Office

<sup>(1)</sup> The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

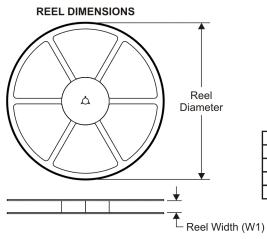
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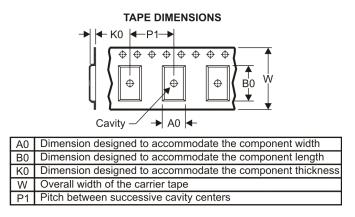


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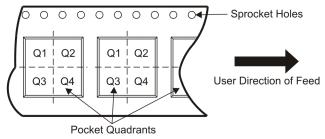
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### TAPE AND REEL INFORMATION





### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE

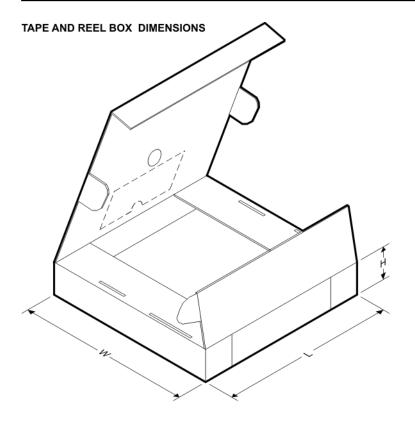


*All dimensions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
NE5534ADR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
NE5534DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
NE5534PSR	SO	PS	8	2000	330.0	16.4	8.2	6.6	2.5	12.0	16.0	Q1
SA5534ADR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SA5534DR	SOIC	D	8	2500	330.0	12.4	6.4	5.2	2.1	8.0	12.0	Q1
SA5534PSR	SO	PS	8	2000	330.0	16.4	8.2	6.6	2.5	12.0	16.0	Q1



# PACKAGE MATERIALS INFORMATION

19-Mar-2008



\*All dimensions are nominal

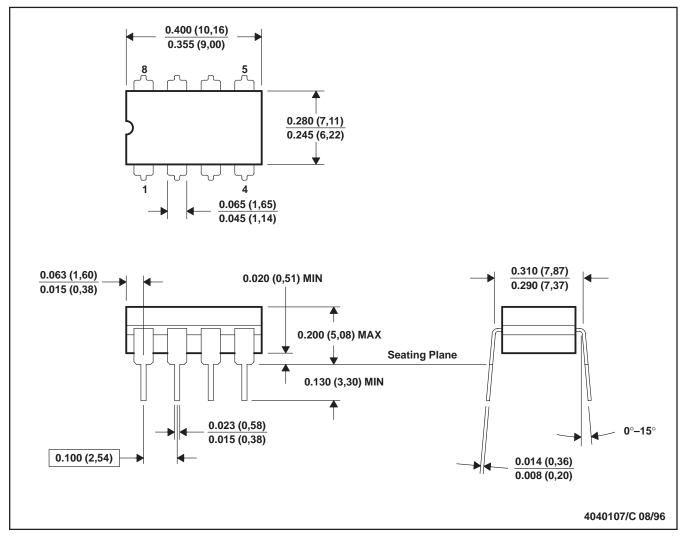
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
NE5534ADR	SOIC	D	8	2500	340.5	338.1	20.6
NE5534DR	SOIC	D	8	2500	340.5	338.1	20.6
NE5534PSR	SO	PS	8	2000	346.0	346.0	33.0
SA5534ADR	SOIC	D	8	2500	340.5	338.1	20.6
SA5534DR	SOIC	D	8	2500	340.5	338.1	20.6
SA5534PSR	SO	PS	8	2000	346.0	346.0	33.0

# **MECHANICAL DATA**

MCER001A - JANUARY 1995 - REVISED JANUARY 1997



#### **CERAMIC DUAL-IN-LINE**



NOTES: A. All linear dimensions are in inches (millimeters).

- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification.
- E. Falls within MIL STD 1835 GDIP1-T8



P(R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE

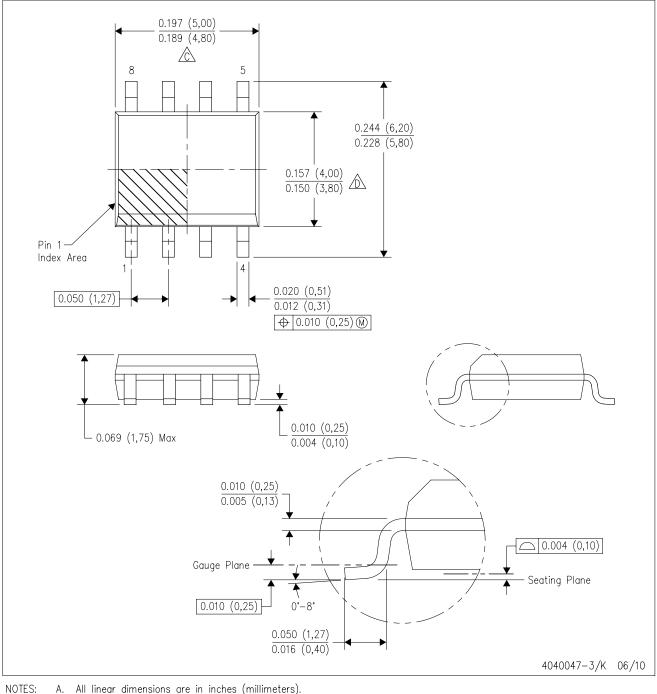


- A. All linear dimensions are in inches (millimeters).B. This drawing is subject to change without notice.
- C. Falls within JEDEC MS-001 variation BA.



D (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



B. This drawing is subject to change without notice.

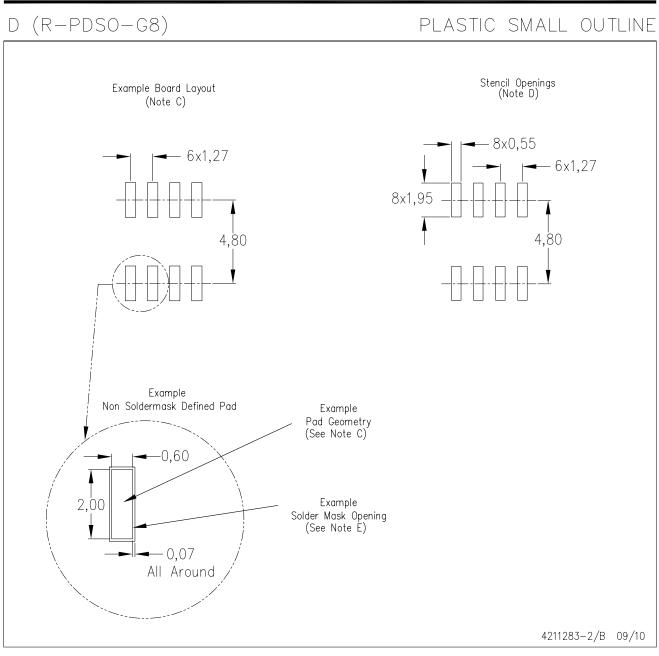
🖄 Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.

Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.

E. Reference JEDEC MS-012 variation AA.



# LAND PATTERN DATA



NOTES: A. All linear dimensions are in millimeters.

- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
  E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



#### **MECHANICAL DATA**

### PS (R-PDSO-G8)

PLASTIC SMALL-OUTLINE PACKAGE



A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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